

FIG. 1

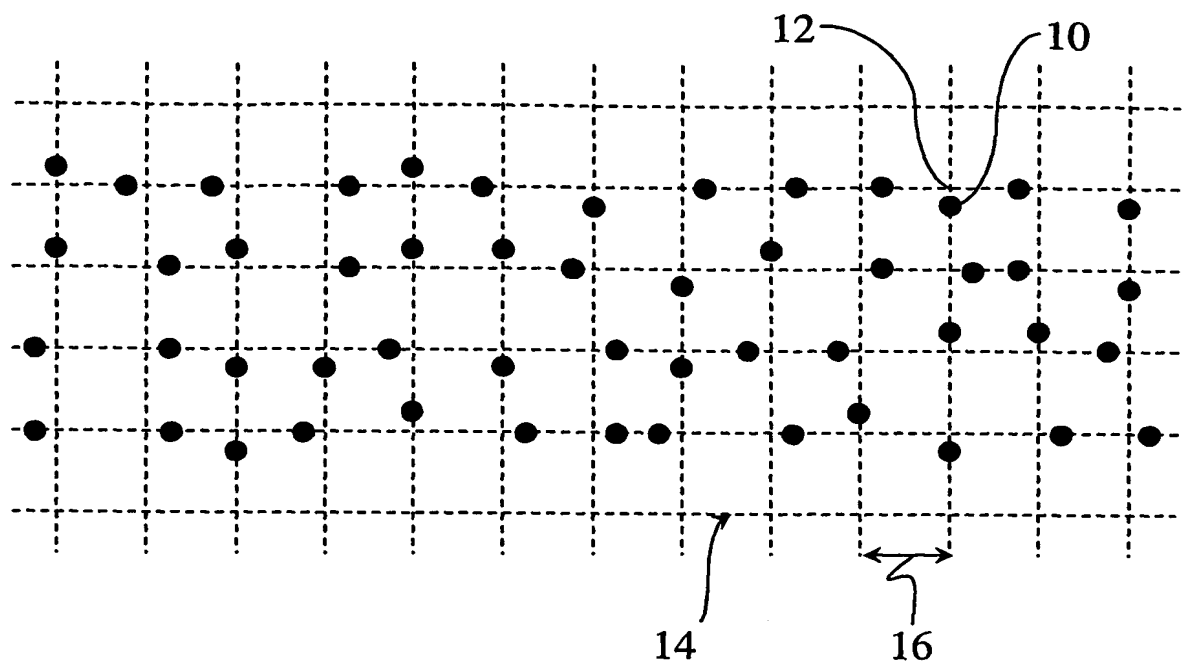


FIG. 2A

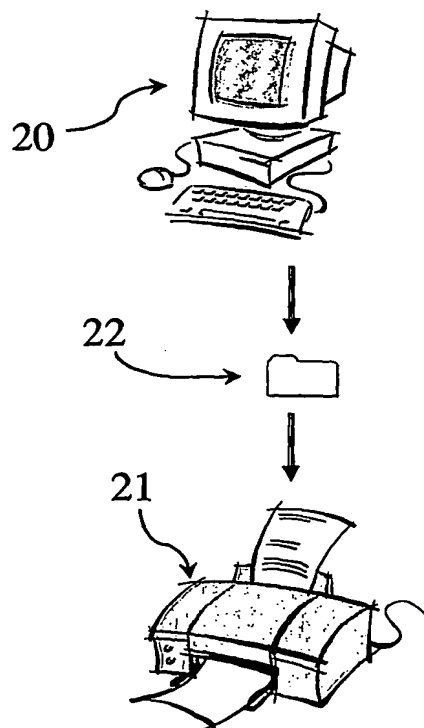


FIG. 2B

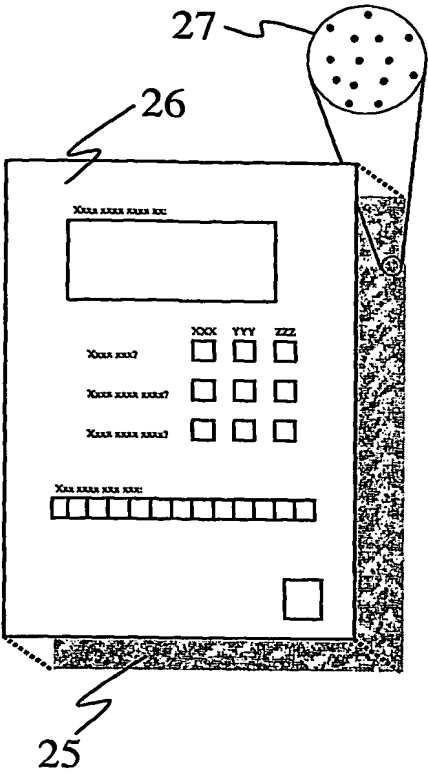


FIG. 3

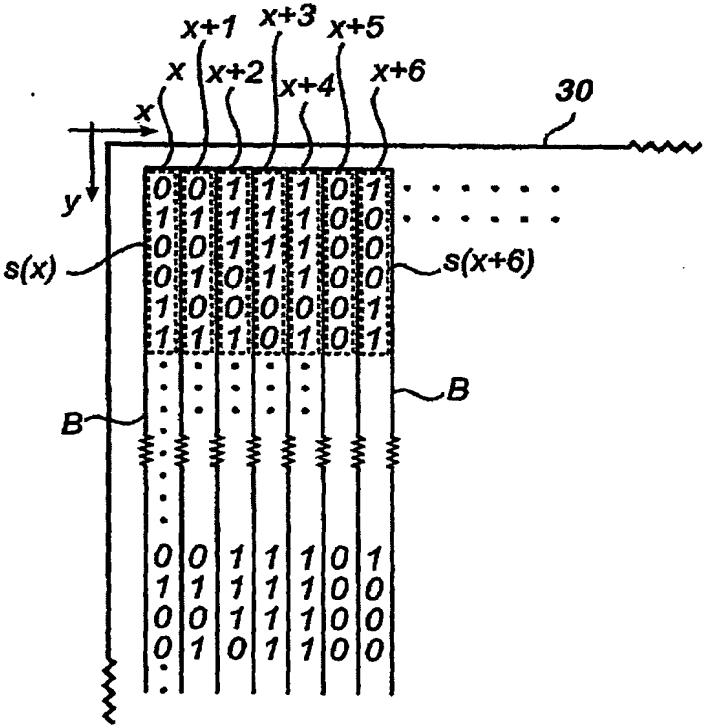


FIG. 4A

Value	Displacement	Pair of bits
0	Upwards	0,0
1	Right	0,1
2	Left	1,0
3	Downwards	1,1

FIG. 4B

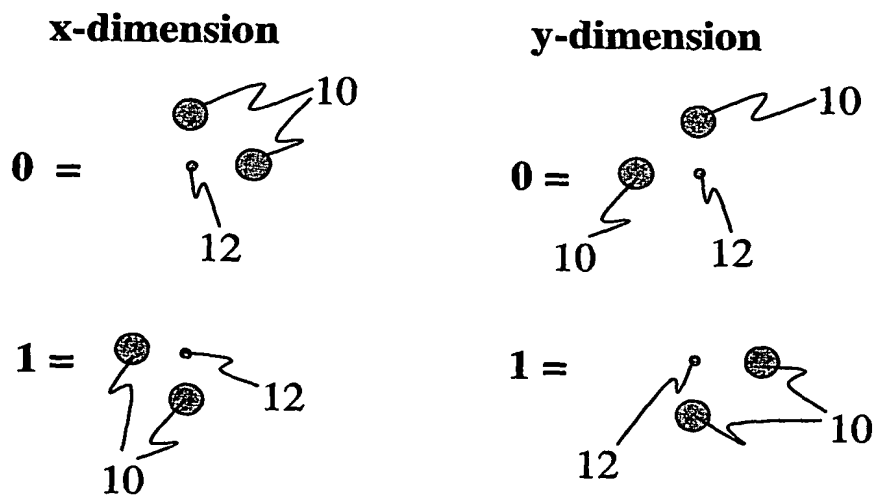


FIG. 4C

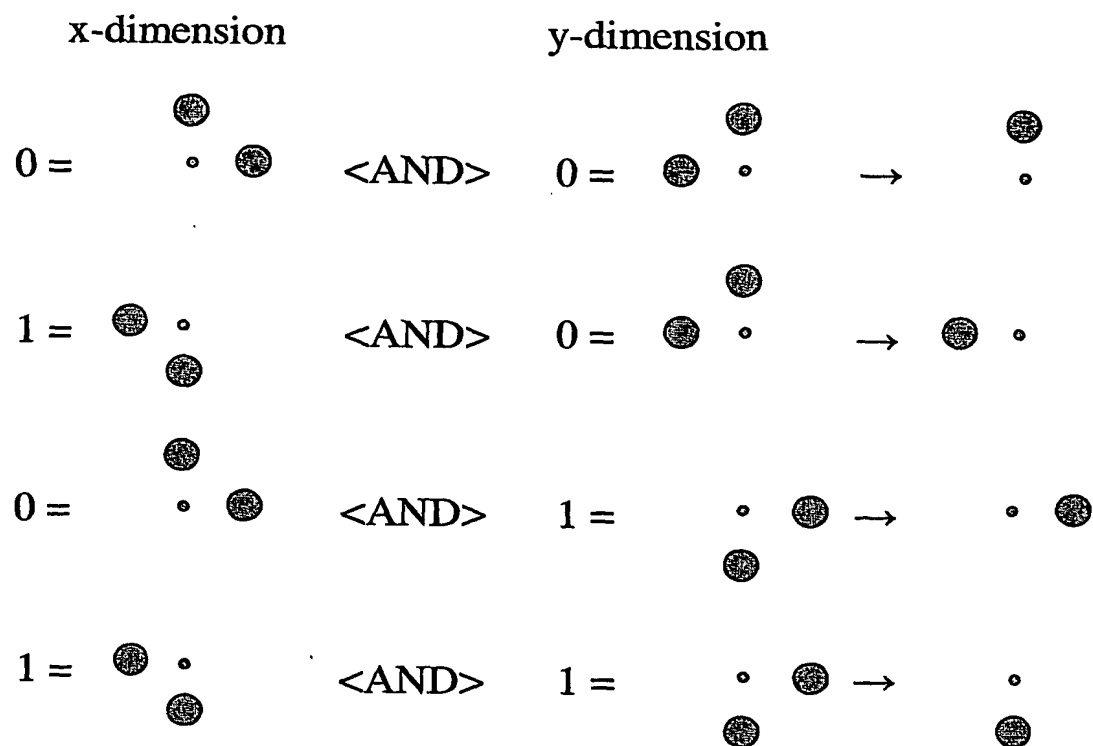


FIG. 4D

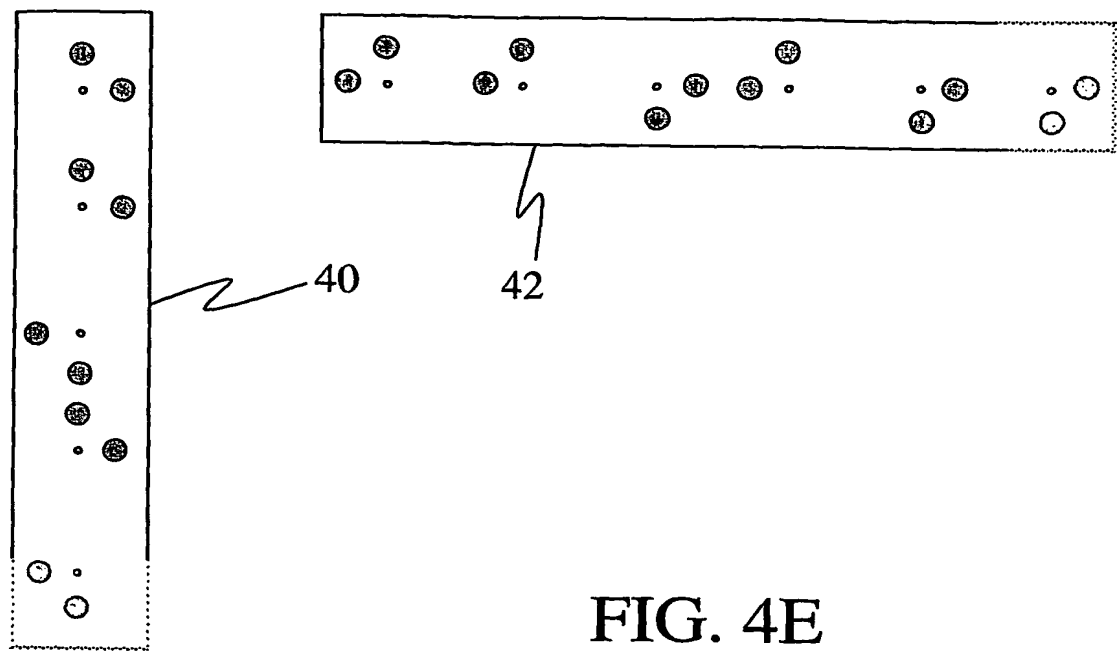


FIG. 4E

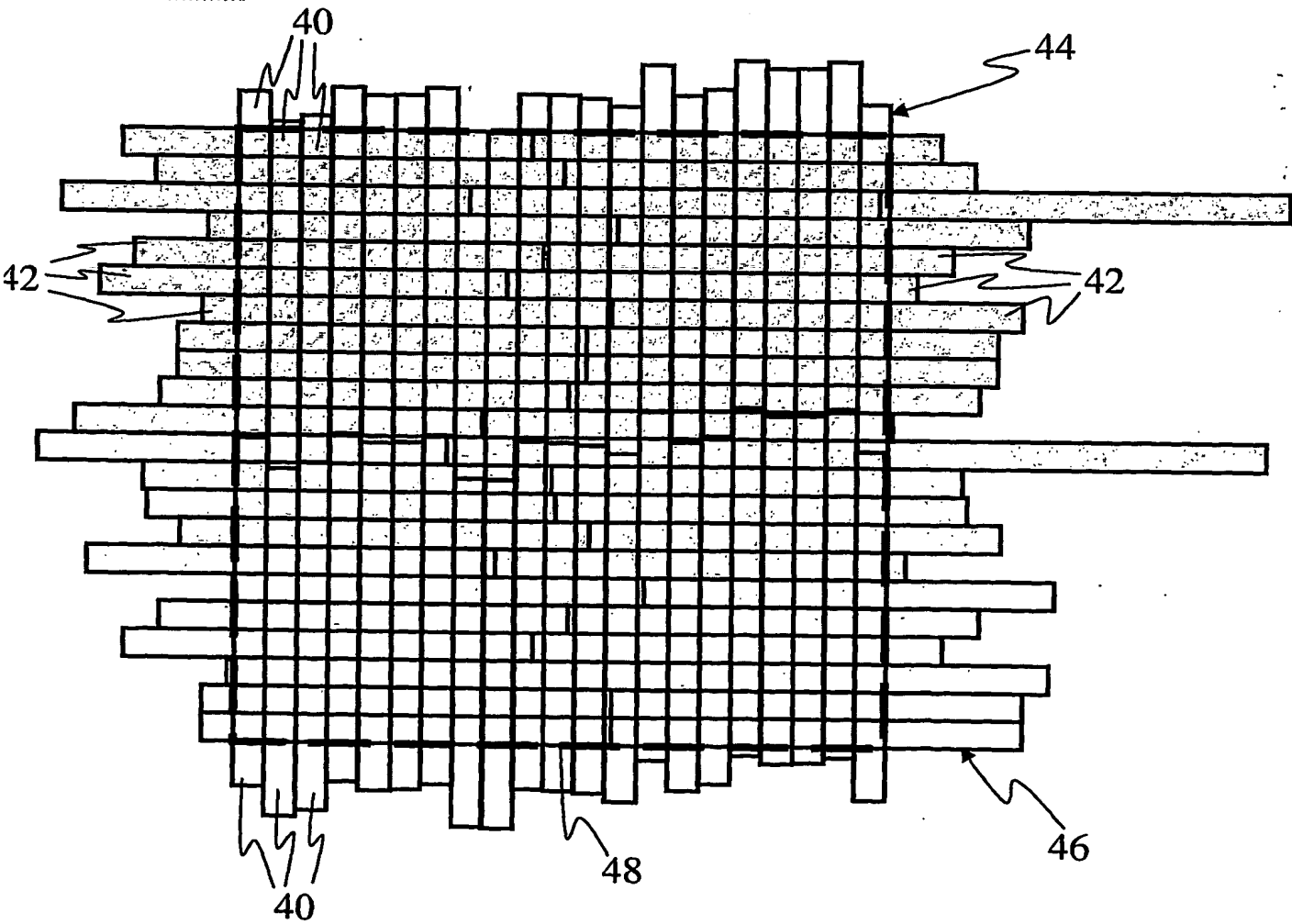


FIG. 5

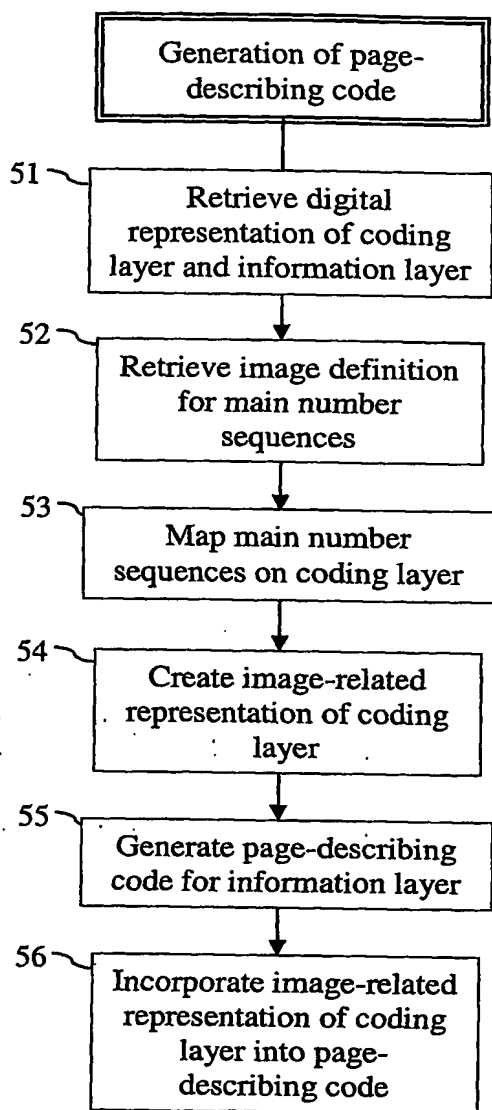


FIG. 6

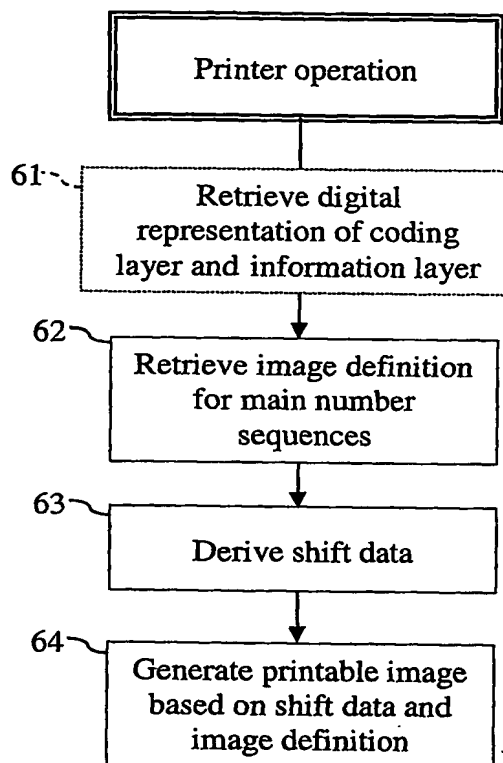


FIG. 7

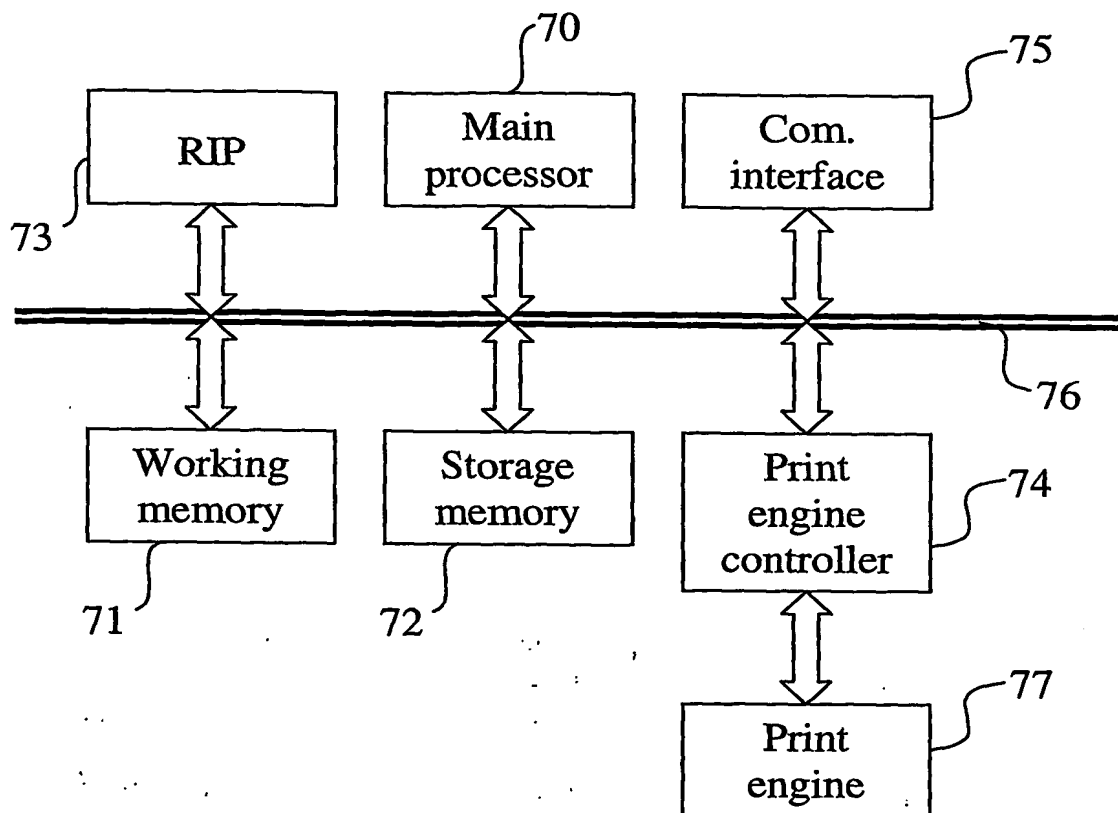


FIG. 8A

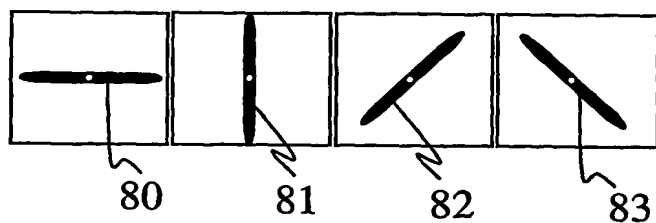


FIG. 8B

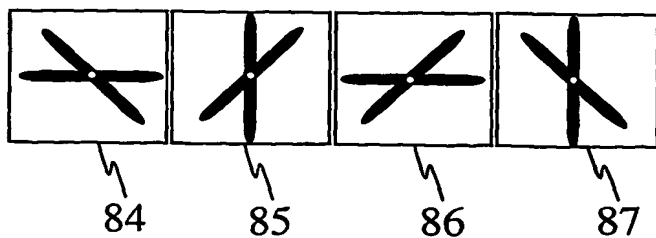


FIG. 9A

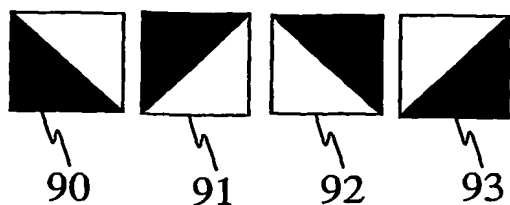


FIG. 9B

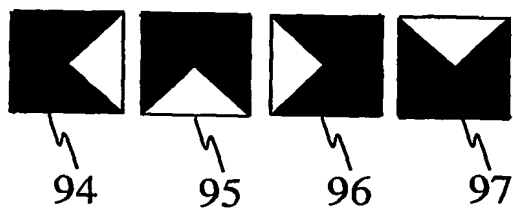


FIG. 10

